## **VLSI DESIGN LAB**

The VLSI Design Laboratory is established with the objective of providing the undergraduate students with an in-depth knowledge IC Design. It offers basic and advanced facilities in the area of VLSI device modeling and simulation using industry standard EDA tools. This Laboratory introduces students to VLSI design and analysis techniques for Electronics Engineers and which serve as solid foundation for cutting edge technology in circuit design. Also the students are trained to design digital circuits using the various hardware description languages and also their implementation on FPGAs.

## VLSI Laboratory is equipped with the following equipment.

S.No	PAGE NO	Name of the Equipment	Available Number
1	50	VLSI Kit (Xilinx Spartan 3E)	1
2	107	Color Television	1
3	111	Standard LAN Trainer Kit	4
4	117	200MHz Mixed Domain Oscilloscope	2
5	123	1) XILINX VIVADO DESIGN SUITE	1
		2) Zed board SOC Development Board SD SOC Key License	2
6	131	ARM, Cortex M4 Board	7
		Raspberry Pi Modal3B+	3
7	136	NEXYS A7: FPGA Board	5
		ZYBO ZYNO: 7000ARM(Xilinx ARM - 7 FPGA)	5
8	140	Basys 3 (Artix - 7) FPGA Trainer Board (Xilinx ARM- 7 FPGA)	12
9		Computer	30