

Faculty Profile



Name of the Faculty : VINOD.A

Designation : ASSISTANT PROFESSOR

Department: ELECTRONICS AND COMMUNICATION ENGINEERING

Date of Joining : 16-05-2017

Faculty ID AICTE ID: 1-3536250658
ANNA UNIVERSITY ID: 6135125

E-Mail Address : Vinodnash@gmail.com

Qualification : M.E

Specialization : VLSI Design

Professional Experience: 12 Years 5 Months

Publications:

(i) International Journals : **1. Warning alert for the fishermen on the Indian maritime boundaries using radio frequency and Zigbee in International Journal of Recent Scientific**

Research Vol 7, Issue, 2, pp. 8758-8761, February, 2016 Mahalakshmi V, Anto Bennet M, Murugan C, Vinod A, Immanuel S and Janakiraman G

2. **Investigation of Effect on Voltage Crump Phenomenon in Power System Circuit for Stagnant Synchronous Compensator Australian Journal of Basic and Applied Sciences** 8(16) October 2014, Pages 34-39 V. Karthikeyan, Dr. T.V.P. Sundarajan, V.J. Vijayalakshmi, A. Vinod and M. Menaka
An Energy Efficient Neighbour Node Discovery Method for Wireless Sensor Networks A. Vinod, V. Karthikeyan and P. Jeyakumar

(iv) National Conferences:

1. Presented paper entitled Implementation of Asynchronous Null Conventional logic circuits using FPGA Logic Element in the National Conference RETEEICOM '09 on 24.4.09 at Mahendra College of Engineering Namakkal

Conferences/Workshops/ Seminar/
Short Term Refresher Courses
Attended (Proof to be Enclosed)

1. Attended Online Workshop on Basics of MATLAB and its Engineering Applications at PES's Modern College of Engineering, Pune from 21.05.2020 to 22.05.2020
2. Attended One week NPTEL-AICTE Faculty Development Programme on CMOS Digital VLSI Design from JANUARY 2020 to MARCH 2020
3. Attended three weeks Industrial Training Program) Communication and Networking Technologies from 01-06-2020 to 19-06-2020 conducted by RGMTC, BSNL, Meenambakkam, Chennai.
4. Attended two day Online Virtual FDP on "Design, Develop and Deliver online courses through MOODLE platform" held at DOTE & CIT Coimbatore from 23.4.2020 to 24.4.2020.
5. Attended National Level E Quiz on Basic Electronics conducted by Government Polytechnic College Udhamandalam on 7.5.2020
6. Attended Short term Training Program on Verilog Program and Implementation in FPGA at NITTTR Chennai from 20.05.2019 to 24.05.2019

7. Attended Short term Training Program on Effective Teaching conducted by NITTTR Chennai from 15.07.2019 to 19.07.2019.
8. Attended a Workshop on ARM Technology and INTEL IoT by Centre of excellence in ARM Technology with RMK Engineering College from 2^{9th} to 3^{1st} OCT 2015.
9. Attended a Workshop on Signals and Systems under the National Mission on Education through ICT conducted by IIT Kharagpur from 2nd to 12th Jan 2014 held at Dr. Mahalingam College of Engineering.
10. Attended a FDP on Presentation Skills for Effective Teaching held on 20th March 2013.
11. Attended a National Seminar on Current Research topics in VLSI Design and Communication conducted by Karunya University Coimbatore on 14th to 15th Dec 2012.
12. Attended a two day National level Workshop on IC System Design at Karunya University Coimbatore on 24 to 25th Feb 2012.
13. Attended a three days STC on HDL Package in Bannari Amman Institute of technology on 11th to 13 Oct 2010.
14. Attended a One day Workshop on Low power VLSI Design Methodologies in Hindusthan Institute of technology on 24th Apr 2010.
15. Attended a One day Workshop on VLSI Signal processing in Sri Krishna College of Engineering Coimbatore on 22nd August 2009.
16. Attended a One day Workshop on Electromagnetic Simulation tools in Bannari Amman Institute of Technology on 27th Jan 2010.
17. Attended the AICTE Sponsored Staff Development Programme titled “Modern trends in VLSI and its applications” in TamilNadu College of Engineering Coimbatore on 08.12.2009.
18. Attended AICTE Sponsored Staff Development Programme on “Recent trends in

Bio-Medical Instrumentation” in TamilNadu College of Engineering Coimbatore.
19. Attended Faculty Development Programme titled “EC 6403-Electromagnetic Fields” in Anna University Tirunelveli from 03.12.2018 to 15.12.2018.

Conferences/Workshops/ Seminars
Organized

1. Eight weeks Module 1 –Orientation towards Technical Education and Curriculum aspects conducted by NITTT Chennai
2. Eight weeks Module 2 –Professional Ethics and Sustainability conducted by NITTT Chennai
3. Eight weeks Module 3 –Communication skill, Modes and Knowledge Dissemination conducted by NITTT Chennai
4. Eight weeks Module 4 –Instructional Planning and delivery conducted by NITTT Chennai
5. Eight weeks course on CMOS Digital VLSI design conducted by NPTEL-IIT Roorkee.
6. Eight weeks course on Discrete time signal Processing conducted by NPTEL-IIT Roorkee.

Membership in professional body:

1. One of the Lifetime members in ISTE.

Other Contributions (If any)

1. Delivered a talk on “Verilog Programming fundamentals” for third year ECE students in Sree Shakthi institute of Engineering and Technology Coimbatore on 06.02.2021.

2. Secured 100% result in Data Converters for IV year, VIII semester ECE during the academic year 2019-2020 at Government College of Engineering Dharmapuri.
3. Secured 100% result in Professional Ethics in Engineering for IV year, VII semester, and ECE-C section during the academic year 2014-2015 at RMD Engineering college.
4. Secured 100% result in Genetic Algorithm for I year, II semester, ECE section during the academic year 2012-2013 at SVS College of Engineering
5. Delivered a talk on “Testability of VLSI” AICTE Sponsored Staff Development Programme on “Modern trends in VLSI and its applications” in Tamilnadu college of engineering Coimbatore.
6. Have Secured a GATE score 392 and all India rank was 11494 in the year 2012.
7. Have Secured a GATE score 425 and all India rank was 18174 in the year 2014
8. Completed an Examination in the following a course on Internal auditing of quality Management System ISO 9001 2008 on 12th and 13th June 2013.